Amendments to the Specification

Please replace paragraph [00022] with the following amended paragraph:

[00022]   Referring to Figure 1, a user 10 is connected to a transmit node 20. The transmit node 20 is connected to receive node 30 by way of ~~"low" speed~~ "low speed" links 40A, 40B, 40C, 40D, 40E. The receive node 30 is coupled to a high speed link 50. It should be noted that the terms "high speed" and "low speed" are relative to each other and are only used for illustration purposes. Thus, each of the links 40A, 40B, 40C, 40D, 40E can be T1 lines transmitting at 1.544 mbps while the link 50 can be a T3 line operating at around 45 mbps. It should be noted that while T1 lines are used in this example, other types and speeds of links may be used.

Please replace paragraph [00023] with the following amended paragraph:

[00023]   In operation, the bundling solution works thus: the user 10 transmits a packet for eventual transmission on the link 50. The packet is received by the transmit node ~~30~~ 20 and is divided into fragments. These fragments can then be independently transmitted via lines 40A, 40B, 40C, 40D, 40E to the receive node 30. The receive node 30 then reassembles the received fragments to recreate the original packet. This original packet is then transmitted through the link 50 to its eventual destination.

Please replace paragraph [00035] with the following amended paragraph:

[00035]   The fragment rebuilding section ~~16~~ 160 controls another memory bank 220. The fragment rebuilding section 160 receives chunks of data from the partial packet processor 200 and stores these in its RAM bank 220. Fragments that have been divided for transport between the transmit node 20 and the receive node 30 are rebuilt in this section. The fragment rebuilding section has logic which examines the data in the received chunks and recognizes the beginning and the end sections of a fragment. One method of accomplishing this is by having a set fragment size. The beginning section is easy to find as it contains the fragment headers. Once this section has been received, a running total of the accumulated fragment sections for a given fragment will provide the size of the fragment. Once the set fragment size is reached, the fragment is considered complete. Alternatively, a flag may be set in the final fragment section to denote that a fragment has been completed.

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Please replace paragraph [00067] with the following amended paragraph:

[00067]   It should be noted that the storage of data in the internal RAM 330 is, again, similar to that used for the RAM 210 in Fig 4 3. Specific areas of the internal RAM 330 are set aside for specific channels. Thus, if the transmit HDLC engine 320 needs to send data for channel A then the memory area for that channel is known and segregated.

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